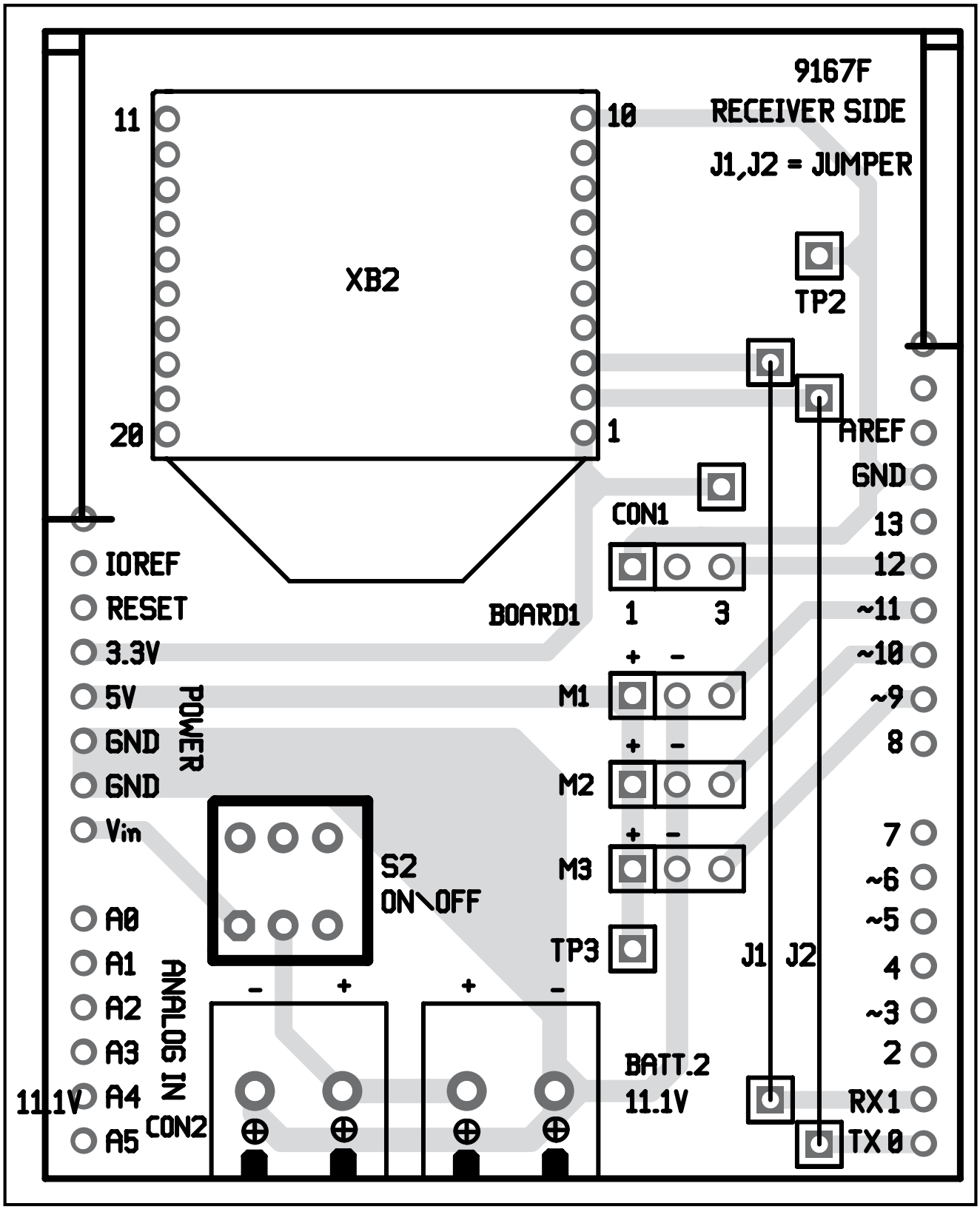

Fig. 10: Component layout for the PCB (receiver side)